

*Materials* **2013**, *6*, 5047-5057; doi:10.3390/ma6115047

OPEN ACCESS

*materials*

ISSN 1996-1944

[www.mdpi.com/journal/materials](http://www.mdpi.com/journal/materials)

Article

## Crystallization of Electrodeposited Germanium Thin Film on Silicon (100)

Mastura Shafinaz Zainal Abidin <sup>1,\*</sup>, Ryo Matsumura <sup>2</sup>, Mohammad Anisuzzaman <sup>2</sup>, Jong-Hyeok Park <sup>2</sup>, Shunpei Muta <sup>2</sup>, Mohamad Rusop Mahmood <sup>3</sup>, Taizoh Sadoh <sup>2</sup> and Abdul Manaf Hashim <sup>4,5,\*</sup>

<sup>1</sup> Faculty of Electrical Engineering, Universiti Teknologi Malaysia, Johor, Skudai 81310, Malaysia

<sup>2</sup> Department of Electronics, Kyushu University, 744 Motooka, Fukuoka 819-0395, Japan; E-Mails: [r\\_matsumura@nano.ed.kyushu-u.ac.jp](mailto:r_matsumura@nano.ed.kyushu-u.ac.jp) (R.M.); [m\\_anisu@nano.ed.kyushu-u.ac.jp](mailto:m_anisu@nano.ed.kyushu-u.ac.jp) (M.A.); [j\\_park@nano.ed.kyushu-u.ac.jp](mailto:j_park@nano.ed.kyushu-u.ac.jp) (J.-H.P.); [s\\_muta@nano.ed.kyushu-u.ac.jp](mailto:s_muta@nano.ed.kyushu-u.ac.jp) (S.M.); [sadoh@ed.kyushu-u.ac.jp](mailto:sadoh@ed.kyushu-u.ac.jp) (T.S.)

<sup>3</sup> Faculty of Electrical Engineering, Universiti Teknologi MARA, Selangor, Shah Alam 40450, Malaysia; E-Mail: [nanouitm@gmail.com](mailto:nanouitm@gmail.com)

<sup>4</sup> Malaysia-Japan International Institute of Technology, Universiti Teknologi Malaysia, Jalan Semarak, Kuala Lumpur 54100, Malaysia

<sup>5</sup> MIMOS Berhad, Technology Park Malaysia, Kuala Lumpur 57000, Malaysia

\* Authors to whom correspondence should be addressed; E-Mails: [mastura@fke.utm.my](mailto:mastura@fke.utm.my) (M.S.Z.A.); [abd\\_manaf@ic.utm.my](mailto:abd_manaf@ic.utm.my) (A.M.H.); Tel.: +603-2203-1389 (M.S.Z.A. & A.M.H.); Fax: +603-2203-1266 (M.S.Z.A. & A.M.H.).

*Received: 20 August 2013; in revised form: 21 October 2013 / Accepted: 28 October 2013 /*

*Published: 6 November 2013*

---

**Abstract:** We report the crystallization of electrodeposited germanium (Ge) thin films on n-silicon (Si) (100) by rapid melting process. The electrodeposition was carried out in germanium (IV) chloride: propylene glycol ( $\text{GeCl}_4\text{:C}_3\text{H}_8\text{O}_2$ ) electrolyte with constant current of 50 mA for 30 min. The measured Raman spectra and electron backscattering diffraction (EBSD) images show that the as-deposited Ge thin film was amorphous. The crystallization of deposited Ge was achieved by rapid thermal annealing (RTA) at 980 °C for 1 s. The EBSD images confirm that the orientations of the annealed Ge are similar to that of the Si substrate. The highly intense peak of Raman spectra at  $300\text{ cm}^{-1}$  corresponding to Ge-Ge vibration mode was observed, indicating good crystal quality of Ge. An additional sub peak near to  $390\text{ cm}^{-1}$  corresponding to the Si-Ge vibration mode was also observed, indicating the Ge-Si mixing at Ge/Si interface. Auger electron

spectroscopy (AES) reveals that the intermixing depth was around 60 nm. The calculated Si fraction from Raman spectra was found to be in good agreement with the value estimated from Ge-Si equilibrium phase diagram. The proposed technique is expected to be an effective way to crystallize Ge films for various device applications as well as to create strain at the Ge-Si interface for enhancement of mobility.

**Keywords:** germanium; silicon; electrochemical deposition; rapid melting

---

## 1. Introduction

It is well known that miniaturization of transistors is very helpful in order to increase the performance of the ultra-large-scale-integrated circuits (ULSIs). Nevertheless, continuous miniaturization of transistors down to nano-scale regime tends to create several problems such as gate leakage current, short channel effect, *etc.* According to the International Technology Roadmap for Semiconductors (ITRS) 2009 edition [1], new channel materials with higher carrier mobilities than silicon (Si) are promising to enhance the switching speed of complementary metal oxide semiconductor (CMOS) transistors. Over the past decade, much attention has been paid to germanium (Ge) and III-V semiconductor channels [2,3] as the candidates to fulfill such purposes. Interestingly, these materials can not only be used to fabricate high speed conventional CMOS, but also to fabricate new transistors with different operating principles, such as tunnel field effect transistor (FET) [4], and various kinds of functional devices, such as sensors [5,6], optical devices [7], detectors [8–10] and solar batteries [11]. As reported by Takagi *et al.* [3], co-integration of these functional materials on Si platform seems to offer the present ULSIs with superb multi-functionalities. As a result, growth of single crystalline Ge and III-V materials on Si is becoming a key issue towards the realization of advanced heterogeneous integration on the Si platform.

In this paper, we present the crystallization of electrodeposited Ge on n-Si (100) by applying rapid melting process. In general, the deposition of polycrystalline or amorphous Ge film on Si can be performed by several techniques, such as chemical vapor deposition (CVD) and molecular beam epitaxy (MBE) system. However, these deposition techniques require ultra high vacuum environment and are expensive. On the other hand, low vacuum techniques such as sputtering and thermal or electron beam evaporation are not able to prevent the surface from being contaminated during the deposition which can affect the subsequent achievement of perfect crystallization of Ge. The electrochemical process is considered to be a promising method since it is low cost, simple and gives high deposition rate, which should be beneficial and practical.

There were a few studies that reported the electrodeposition of semiconductor thin films [12,13]. In the electrodeposition technique, electrolytes or medium solvents play an important role in determining the quality of deposited thin films. An electrodeposition of semiconductor materials has been mainly achieved in non-aqueous solvents [12] such as ionic liquids [14–16] and glycols [17,18]. A relatively thick Ge film can be obtained from a non-aqueous bath of a mixture of germanium (IV) chloride (GeCl<sub>4</sub>) or germanium (IV) iodide (GeI<sub>4</sub>) in ethylene glycol. However, Szekely [17] reported that electrodeposition using this ethylene glycol bath shows some difficulties in operation, *i.e.*, high corrosion rate of an anode and eventual precipitation of a white crystalline substance on the substrate.

In contrast, no difficulty of operation was found by using a propylene glycol ( $\text{C}_3\text{H}_8\text{O}_2$ ) bath which enables a deposition of Ge thin film with better controllability of thickness [17]. The first attempt for electrodeposition of Ge from a solution of  $\text{GeCl}_4$  in  $\text{C}_3\text{H}_8\text{O}_2$  was done by Szekely on metal substrates, such as Ni and Cu, at several temperatures ranging from 20 to 80 °C [17,18]. Saitou *et al.* [19] deposited crystalline Ge thin films electrochemically on Cu substrates from  $\text{GeCl}_4$  in  $\text{C}_3\text{H}_8\text{O}_2$  electrolyte at room temperature. Instead of  $\text{GeCl}_4$ , Endres *et al.* [14,15] reported that Ge films could also be obtained from germanium bromide ( $\text{GeBr}_4$ ) by adding suitable solvents. From these reported works, it is proven that an electrodeposition technique can be used to deposit Ge thin films on conductive substrates, including semiconductors. To our knowledge, no experimental work has yet been done by using  $\text{GeCl}_4$  in  $\text{C}_3\text{H}_8\text{O}_2$  electrolyte to deposit Ge films on semiconductor substrates. In this paper, we report for the first time the electrodeposition of Ge films on Si substrates by using a mixture of  $\text{GeCl}_4$  and  $\text{C}_3\text{H}_8\text{O}_2$ .

In the Ge electrodeposition process, the important factor that has to be considered is that the deposited Ge layer should be highly pure without any excessive contamination from other elements such as oxygen. Such contamination will prevent Ge from being crystallized due to the incorporation of oxygen on the Si surface. In general, the as-deposited Ge film by any technique tends to show either amorphous or polycrystalline structure. The crystallization of amorphous Ge has been shown to be achievable by applying a so-called rapid melting process [20–26]. This paper presents the crystallized characteristics of electrodeposited Ge on Si by a rapid melting process.

## 2. Experimental

The electrodeposition was carried out on n-type phosphorus-doped Si (100) substrates with thickness of 355–405  $\mu\text{m}$  and resistivity of 0.7–1.3  $\Omega\text{ cm}$ . Prior to deposition process, the samples were cleaned by standard RCA process and buffered hydrofluoric (BHF) acid to remove native oxide layer. The electrodeposition was carried out in a simple teflon cell by using two terminal configuration where Si substrate was placed at the bottom to act as a cathode and Pt wire was used as an anode. The electrolyte is a mixture of 5%  $\text{GeCl}_4$  in  $\text{C}_3\text{H}_8\text{O}_2$  [19]. The process was done at room temperature with constant current of 50 mA (current density,  $J = 20\text{ mA cm}^{-2}$ ) for 30 min in a nitrogen-filled glove box in order to minimize the effects from oxygen and moisture. After electrodeposition, the samples were immediately immersed in deionized water.

The deposited Ge films were patterned into circular islands with diameter,  $\phi$  of 3, 5, 7, 10, 15 and 20  $\mu\text{m}$  using photolithography and wet etching. A capping layer of  $\text{SiO}_2/\text{SiN}_x$  with thickness of 2  $\mu\text{m}$  was deposited by magnetron sputtering. The purpose of reducing the size of Ge film and introducing capping layer is to avoid severe agglomeration of Ge during the rapid melting process [20,25]. The sample was heat treated by rapid thermal annealing (RTA) at 980 °C for 1 s to realize the crystallization of Ge. The capping layer was removed by wet etching prior to measurement. The samples were characterized using Nomarski microscopy (BX51M, Olympus Corp., Tokyo, Japan), atomic force microscopy (AFM, XE-100, Park Systems Inc., Santa Clara, CA, USA), energy dispersive X-ray spectroscopy (EDS, SU8030, Hitachi, Tokyo, Japan), Raman spectroscopy (Horiba Jobin Yvon HR640, Nd:YVO<sub>4</sub> crystal, 532 nm wavelength, spot size  $\sim 1\text{ }\mu\text{m}\phi$ , Horiba, Kyoto, Japan), Auger electron spectroscopy (AES, JAMP-9500F, JEOL Ltd., Tokyo, Japan) field emission Auger

microprobe, accelerating voltage: 10 kV) and electron backscattering diffraction (EBSD, EBSP JSM-5510LS scanning electron microscopy, JEOL Ltd.).

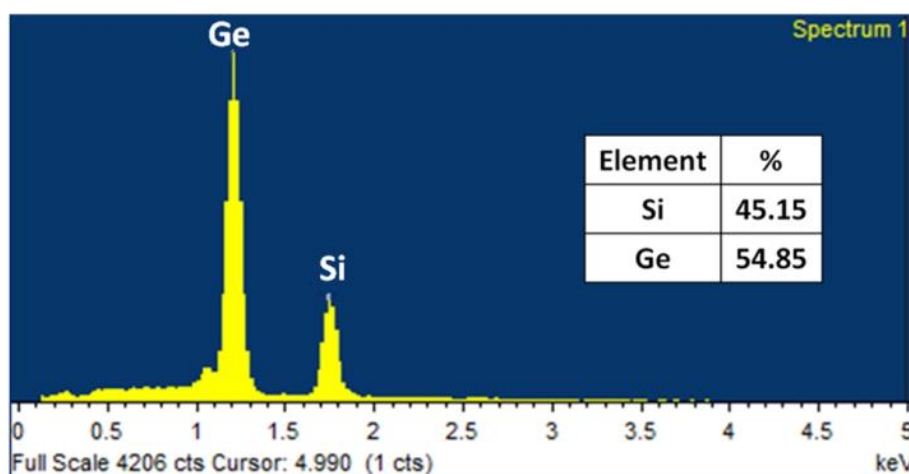
### 3. Results and Discussion

Figure 1a shows the optical image of electrodeposited Ge film on n-Si substrate. The electrodeposited Ge films on n-Si at a current of 50 mA ( $J = 20 \text{ mA cm}^{-2}$ ) always show silver-like bright in color. From the optical image, no specific microstructure was observed, suggesting that the deposited film was uniform. Based on EDS spectra as shown in Figure 1b, the deposited Ge film was found to be highly pure without any excessive contaminants such as oxygen and Pt metal. For this specific sample, the thickness of Ge film measured by surface profiler was around 650 nm. The thinning process was done to reduce the thickness of electrodeposited Ge using a mixture of ammonium hydroxide,  $\text{NH}_4\text{OH}$ : hydrogen peroxide;  $\text{H}_2\text{O}_2$ : deionized water,  $\text{H}_2\text{O}$  (1:7:40). This process was applied in order to minimize the agglomeration of Ge during the rapid melting process. After the thinning process, the thickness of Ge film was around 160 nm. The EBSD analysis was carried out to examine the crystallographic orientation of the as-deposited Ge films on Si substrate. As shown in Figure 1c, random distribution of colors was obtained, indicating Ge film with amorphous structure.

**Figure 1.** (a) Optical microscope images, (b) energy dispersive X-ray spectroscopy (EDS) and (c) electron backscattering diffraction (EBSD) images of as-deposited Ge on Si substrate.

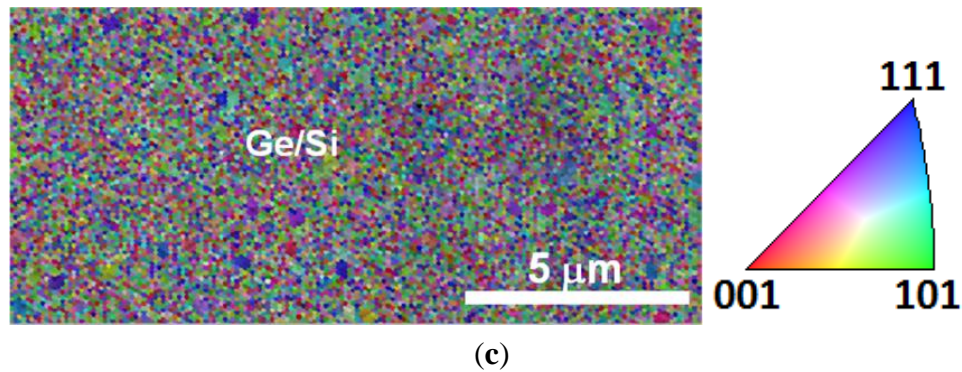


(a)



(b)

Figure 1. Cont.



Further observation using AFM confirmed that uniform and relatively smooth Ge film was successfully electrodeposited on Si substrate. The two-dimensional (2D) and three-dimensional (3D) AFM images of as-deposited and annealed Ge on Si substrate were shown in Figure 2. The surface roughness in root-mean-square (RMS) was found to be 0.828 nm for as-deposited sample and slightly increased to 3.847 nm after the rapid annealing process.

**Figure 2.** Two-dimensional (2D-) and three-dimensional (3D) atomic force microscopy (AFM) images, (a) as-deposited Ge/Si (Scan area =  $10\ \mu\text{m} \times 10\ \mu\text{m}$ , root-mean-square (RMS) = 0.828 nm) and (b) annealed Ge/Si (Scan area =  $5\ \mu\text{m} \times 5\ \mu\text{m}$ , RMS = 3.847 nm).

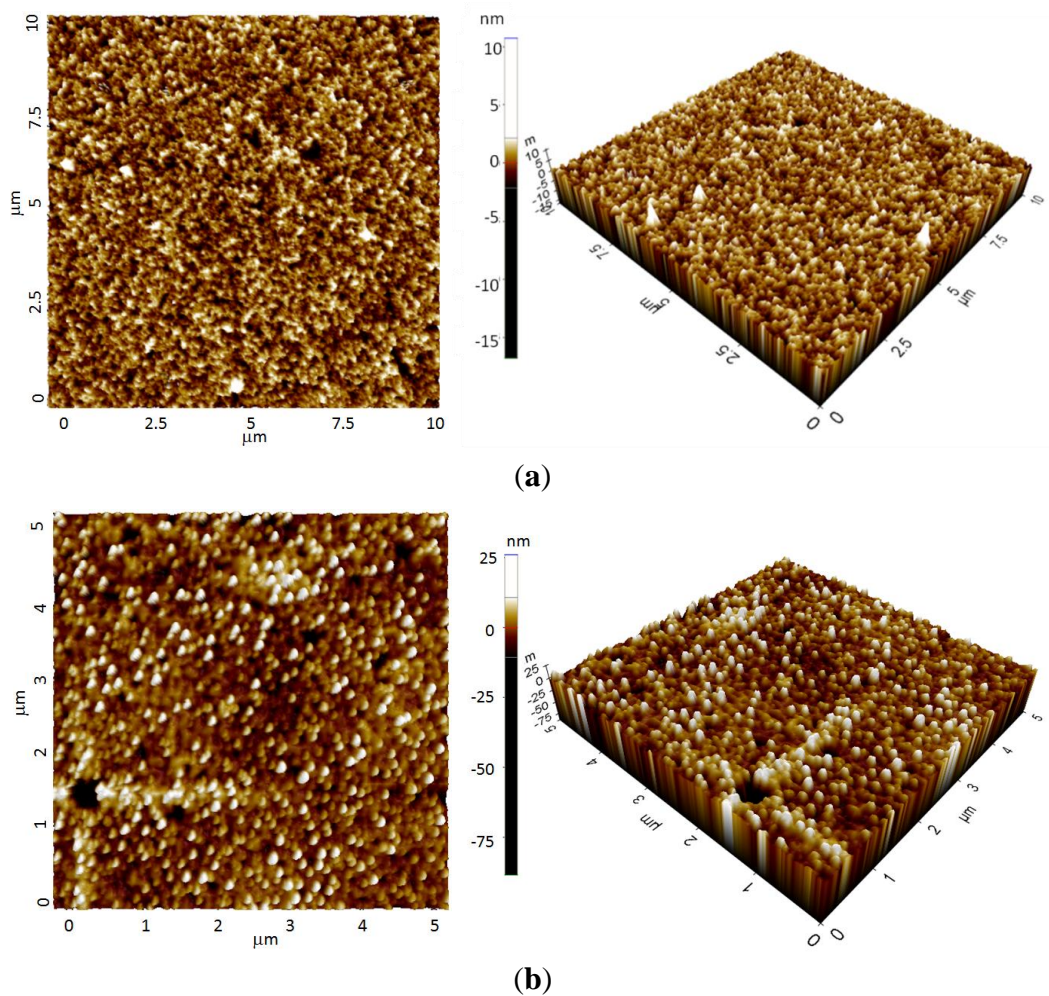
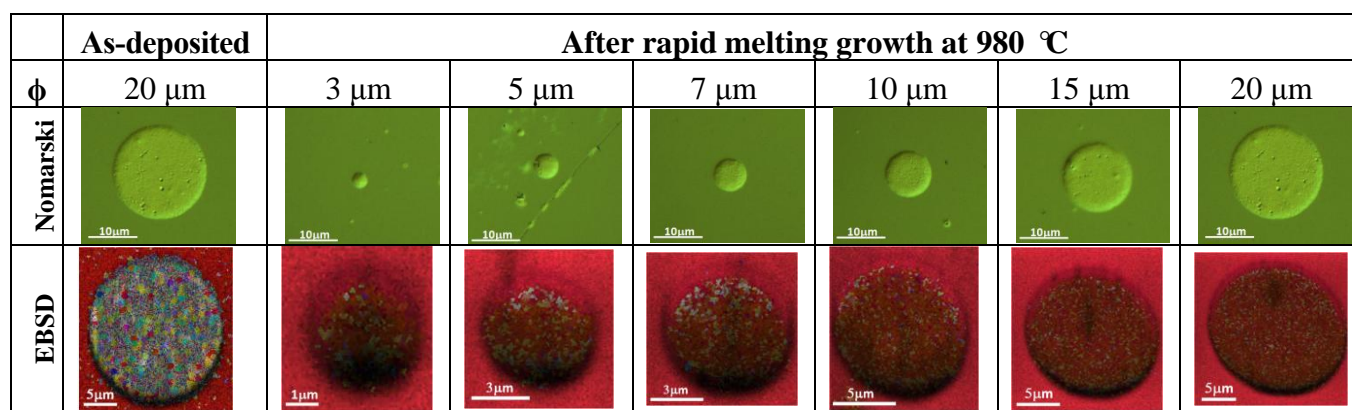




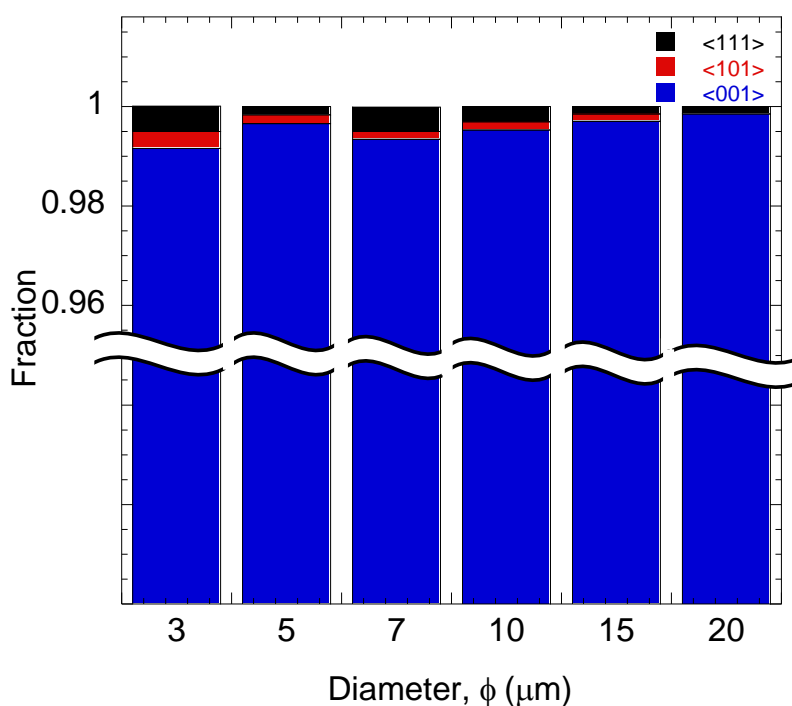
Figure 3 shows the Nomarski and EBSD images of the patterned Ge before and after the annealing process. Here, the capping layer was removed before the EBSD measurements. No agglomeration of Ge was observed, suggesting sufficient suppression by the capping layer. Based on EBSD images, the orientation of the grown Ge were confirmed to be in (100) which is similar to that of Si (100) substrate. As shown in Figure 4, the orientation mapping of the sample surface seems to be dominated by (100) with some minor orientations.

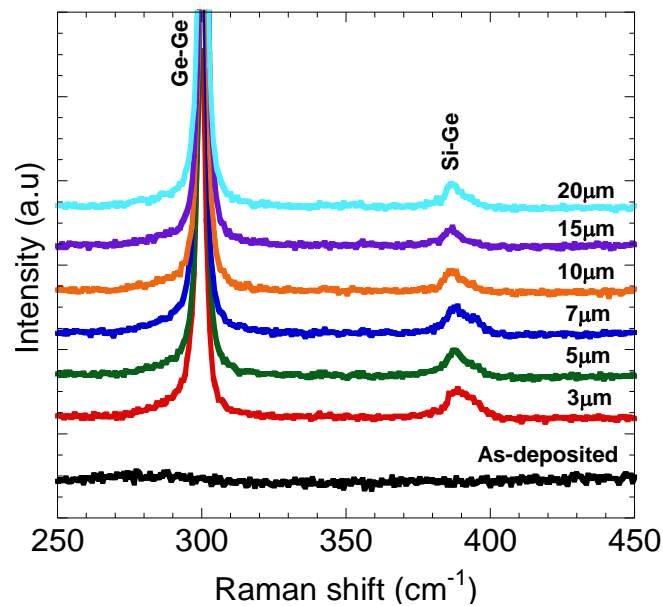
From the measured Raman spectra as shown in Figure 5, no significant Ge-Ge vibration mode peak was observed for the as-deposited Ge, thus confirming that the structure was amorphous. A similar result was also reported by Huang *et al.* [27] where the as-deposited Ge film deposited using 0.5 M  $\text{GeCl}_4$  in 1,3-propanediol electrolyte was found to be amorphous.

**Figure 3.** Nomarski and EBSD images of as-deposited and annealed Ge patterns.



**Figure 4.** Distribution of orientation fraction of Ge patterns.



**Figure 5.** Raman spectra of as-deposited and annealed Ge patterns.

From Raman spectra as shown in Figure 5, the main peak at  $\sim 300\text{ cm}^{-1}$  which corresponds to Ge-Ge vibration mode was clearly observed after rapid annealing process, confirming the crystallization of Ge. The values of full width at half maximum (FWHM) of these peaks were estimated to be around  $3.8\text{--}6.8\text{ cm}^{-1}$ , which was slightly wider than standard single crystalline Ge wafer ( $3.2\text{ cm}^{-1}$ ). However, these values seem to indicate relatively good crystal quality of Ge. The sub peaks ( $\sim 390\text{ cm}^{-1}$ ) due to Si-Ge vibration mode were also observed, indicating that Si-Ge mixing at Ge/Si interface occurred [25]. The diffusion of Si and Ge into each other's regions seems to effectively take place during the melting process, as illustrated in Figure 6.

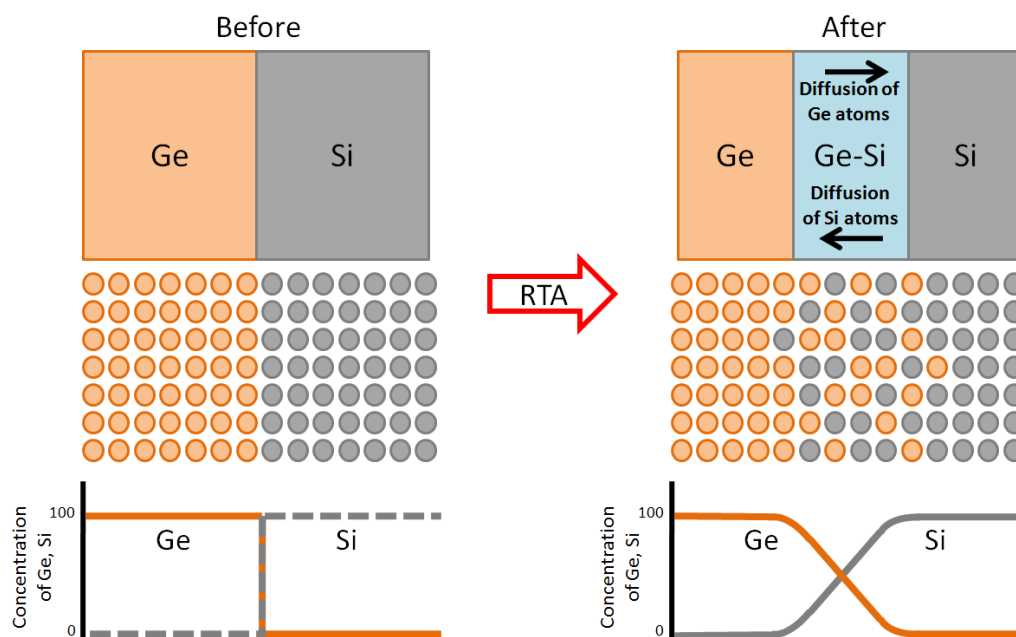
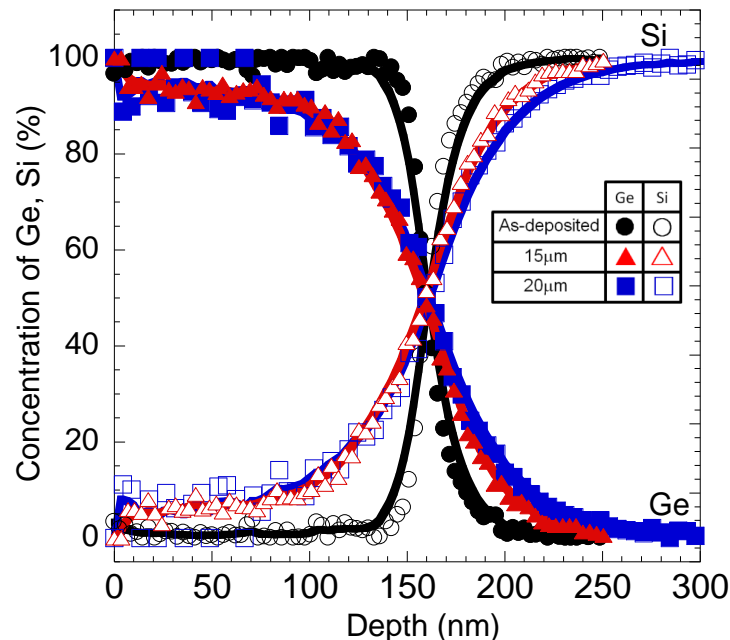
**Figure 6.** Diffusion of Si and Ge.

Figure 7 shows the depth profiles measured by AES for the samples ( $\phi$ : 15 and 20  $\mu\text{m}$ ) before and after annealing. It is found that the intermixing layer with 60 nm thickness was formed by referring to drastic changes in the profile gradient.

**Figure 7.** Depth profiles for sample annealed at 980  $^{\circ}\text{C}$ .

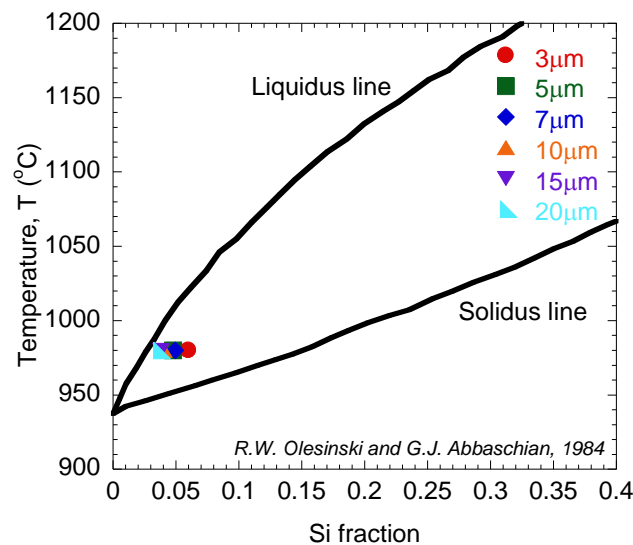


The Si fraction ( $x$ :  $0 \leq x \leq 1$ ) in the surface regions of grown layers was calculated using Equation (1) by referring to the intensity ratio of Ge-Ge and Si-Ge vibration mode peaks obtained from Raman spectra as shown in Figure 5 [28].

$$\frac{I(\text{Ge-Ge})}{I(\text{Si-Ge})} = \frac{k(1-x)}{2x} \quad (1)$$

Here,  $I(\text{Ge-Ge})$  and  $I(\text{Si-Ge})$  are the peak intensities of Raman signals originating from Ge-Ge and Si-Ge vibration modes, respectively, and  $k$  is a constant. The value of  $k$  was determined as 1.6 which was described in [25]. As shown in Figure 8, the calculated Si fractions were plotted into the liquidus-solidus equilibrium diagram. It was found that the Si fraction was around 4% to 5% of all grown layers. These small discrepancies of Si fractions between samples with different Ge diameters could be due to the variation of latent heat distribution during the annealing process. Based on this equilibrium diagram, the points were found to be distributed near to the liquidus line [29]. The slight deviation from the liquidus line can be attributed to Si diffusion just after solidification or slight deviation in the rapid thermal annealing temperature.



**Figure 8.** Si fraction at different  $\phi$ , plotted in Ge-Si equilibrium phase diagram.

For Raman spectroscopy measurement, the laser penetration depth in Ge is approximately 20 nm [30]. Therefore, these fraction values represent the amount of Si diffused into Ge layer up to 20 nm depth from the surface. As shown in Figure 7, the AES depth profile of grown samples shows the clear correlation between the thickness and the amount of Si diffused into the Ge layer. It can be seen that the percentages of Si diffused into Ge layer at 20 nm depth were about 5% to 6%, for both samples with  $\phi = 15$  and 20  $\mu\text{m}$ . These values were almost the same as the calculated values of Si fraction using Equation (1), which were found to be around 4% to 5% for the same samples.

#### 4. Conclusions

In conclusion, the crystallization of electrodeposited amorphous Ge thin films on n-Si (100) by rapid melting process was successfully achieved. The intermixing region was around 60 nm from the Ge-Si interface. The proposed technique is expected to not only be an effective way to crystallize Ge films for various device applications but also to create strain at the Ge-Si interface for the enhancement of mobility.

#### Acknowledgments

Mastura Shafinaz Zainal Abidin thanks Universiti Teknologi Malaysia (UTM) and the Ministry of Higher Education Malaysia (MOHE) for the financial supports during her internship at Kyushu University, Japan. The authors wish to extend their thanks for the measurement facilities provided by the Department of Electronics, Kyushu University. This work is partly sponsored by NSG grant from Nippon Sheet Glass Corp., RU grant and MJIIT grant from UTM, FRGS grant and ERGS grant from MOHE.

#### Conflicts of Interest

The authors declare no conflict of interest.

## References

1. International Technology Roadmap for Semiconductors. Available online: <http://public.itrs.net/links/2009ITRS/Home2009.htm> (accessed on 31 August 2012).
2. Bescond, M.; Cavassilas, N.; Kalna, K.; Nehari, K.; Raymond, L.; Autran, J.L.; Lannoo, M.; Asenov, A. Ballistic Transport in Si, Ge and GaAs Nanowire MOSFETs. In Proceedings of Electron Devices Meeting, Washington, DC, USA, 5 December 2005; pp. 526–529.
3. Takagi, S.; Sugiyama, M.; Yasuda, T.; Takenaka, M. Ge/III-V Channel Engineering for Future CMOS. In *Graphene and Emerging Materials for Post-CMOS Applications*; Obeng, Y.; DeGendt, S., Srinivasan, P., Misra, D., Iwai, H., Karim, Z., Hess, D.W., Grebel, H., Eds.; Electrochemical Society Inc: Pennington, Australia, 2009; pp. 9–20.
4. Boucart, K.; Ionescu, A.M. Double-gate tunnel FET with high-k gate dielectric. *IEEE Trans. Electron Devices* **2007**, *54*, 1725–1733.
5. Liu, Y.; Gopalafishan, K.; Griffin, P.B.; Ma, K.; Deal, M.D.; Plummer, J.D. MOSFETs and High-Speed Photodetectors on Ge-on-Insulator Substrates Fabricated Using Rapid Melt Growth. In IEEE International Electron Devices Meeting, San Francisco, CA, USA, 13–15 December 2004; pp. 1001–1004.
6. Abidin, M.S.Z.; Hashim, A.M.; Sharifabad, M.E.; Rahman, S.F.A.; Sadoh, T. Open-gated pH sensor fabricated on an undoped-AlGaIn/GaN HEMT structure. *Sensors* **2011**, *11*, 3067–3077.
7. Itabashi, S.; Nishi, H.; Tsuchizawa, T.; Watanabe, T.; Shinojima, H.; Park, S.; Yamada, K.; Ishikawa, Y.; Wada, K. Integration of Optical Devices Based on Si, Ge and SiO<sub>x</sub>. In 2010 7th IEEE International Conference on Group IV Photonics (GFP), Beijing, China, 1–3 September 2010; pp. 48–50.
8. Wang, J.; Lee, S. Ge-Photodetectors for Si-based optoelectronic integration. *Sensors* **2011**, *11*, 696–718.
9. Hashim, A.M.; Mustafa, F.; Rahman, S.F.A.; Rahman, A.R.A. Dual-functional on-chip AlGaAs/GaAs schottky diode for RF power detection and low-power rectenna applications. *Sensors* **2011**, *11*, 8127–8142.
10. Hashim, A.M.; Kasai, S.; Hasegawa, H. Observation of first and third harmonic responses in two-dimensional AlGaAs/GaAs HEMT devices due to plasma wave interaction. *Superlattices Microst.* **2008**, *44*, 754–760.
11. Claeys, C.; Simoen, E. *Germanium-Based Technologies: From Materials to Devices*; Elsevier Science: Amsterdam, The Netherlands, 2007; p. 480.
12. Lokhande, C.D.; Pawar, S.H. Electrodeposition of thin film semiconductors. *Phys. Status Solidi A* **1989**, *111*, 17–40.
13. Jayakrishnan, S.; Pushpavanam, M.; Shenoi, B.A. Electrodeposition from organic solutions of metals that are difficult to deposit from aqueous solutions. *Surf. Technol.* **1981**, *13*, 225–240.
14. Endres, F.; Abedin, S.Z.E. Nanoscale electrodeposition of germanium on Au(111) from an ionic liquid: An *in situ* STM study of phase formation. *Phys. Chem. Chem. Phys.* **2002**, *4*, 1640–1657.
15. Endres, F. Ionic liquids: Promising solvents for electrochemistry. *Z. Phys. Chem.* **2004**, *218*, 255–283.

16. Mukhopadhyay, I.; Freyland, W. Thickness induced metal–nonmetal transition in ultrathin electrodeposited Ge films. *Chem. Phys. Lett.* **2003**, *377*, 223–228.
17. Szekely, G. Electrodeposition of germanium. *J. Electrochem. Soc.* **1951**, *98*, 318–324.
18. Szekely, G. Electroplating of Germanium. US2690422 A, 1950.
19. Saitou, M.; Sakae, K.; Oshikawa, W. Evaluation of crystalline germanium thin films electrodeposited on copper substrates from propylene glycol electrolyte. *Surf. Coat. Technol.* **2002**, *162*, 101–105.
20. Hashim, A.M.; Anisuzzaman, M.; Muta, S.; Sadoh, T.; Miyao, M. Epitaxial-template structure utilizing Ge-on-insulator stripe arrays with nano-spacing for advanced heterogeneous integration on Si platform. *Jpn. J. Appl. Phys.* **2012**, *51*, doi:10.1143/JJAP.51.06FF04.
21. Liu, Y.; Deal, M.D.; Plummer, J.D. High-quality single-crystal Ge on insulator by liquid-phase epitaxy on Si substrates. *Appl. Phys. Lett.* **2004**, *84*, 2563–2565.
22. Tweet, D.J.; Lee, J.J.; Maa, J.-S.; Hsu, S.T. Characterization and reduction of twist in Ge on insulator produced by localized liquid phase epitaxy. *Appl. Phys. Lett.* **2005**, *87*, 141908:1–141908:3.
23. Gao, F.; Lee, S.J.; Balakumar, S.; Du, A.; Foo, Y.-L.; Kwong, D.-L. Ge diffusion and solid phase epitaxy growth to form  $\text{Si}_{1-x}/\text{Ge}_x/\text{Si}$  and Ge on insulator structure. *Thin Solid Films* **2006**, *504*, 69–72.
24. Balakumar, S.; Roy, M.M.; Ramamurthy, B.; Tung, C.H.; Fei, G.; Tripathy, S.; Dongzhi, C.; Kumar, R.; Balasubramanian, N.; Kwong, D.L. Fabrication aspects of germanium on insulator from sputtered Ge on Si-substrates. *Electrochem. Solid State Lett.* **2006**, *9*, G158–G160.
25. Miyao, M.; Tanaka, T.; Toko, K.; Tanaka, M. Giant Ge-on-insulator formation by Si–Ge mixing-triggered liquid-phase epitaxy. *Appl. Phys. Express* **2009**, *2*, doi: 10.1143/APEX.2.045503.
26. Toko, K.; Sakane, T.; Tanaka, T.; Sadoh, T.; Miyao, M. Defect-free single-crystal Ge island arrays on insulator by rapid-melting-growth combined with seed-positioning technique. *Appl. Phys. Lett.* **2009**, *95*, 112107:1–112107:3.
27. Huang, Q.; Bedell, S.W.; Saenger, K.L.; Copel, M.; Deligianni, H.; Romankiwa, L.T. Single-crystalline germanium thin films by electrodeposition and solid-phase epitaxy. *Electrochem. Solid State Lett.* **2007**, *10*, D124–D126.
28. Mooney, P.M.; Dacol, F.H.; Tsang, J.C.; Chu, J.O. Raman scattering analysis of relaxed  $\text{Ge}_x\text{Si}_{1-x}$  alloy layers. *Appl. Phys. Lett.* **1993**, *62*, 2069–2071.
29. Olesinski, R.W.; Abbaschian, G.J. The Ge–Si (Germanium–Silicon) system. *Bull. Alloy Phase Diagr.* **1984**, *5*, 180–183.
30. Strain Measurements of a Si Cap Layer Deposited on a SiGe Substrate Determination of Ge Content. Available online: [http://www.intercovamex.com/biblioteca\\_de\\_aplicaciones/SEMICONDUCTORES/Mediciones%20de%20tension%20de%20de%20una%20capa%20de%20Si%20depositada%20en%20un%20sustrato%20de%20SiGe.%20Determinacion%20del%20contenido%20de%20Ge.pdf](http://www.intercovamex.com/biblioteca_de_aplicaciones/SEMICONDUCTORES/Mediciones%20de%20tension%20de%20de%20una%20capa%20de%20Si%20depositada%20en%20un%20sustrato%20de%20SiGe.%20Determinacion%20del%20contenido%20de%20Ge.pdf) (accessed on 9 June 2012).